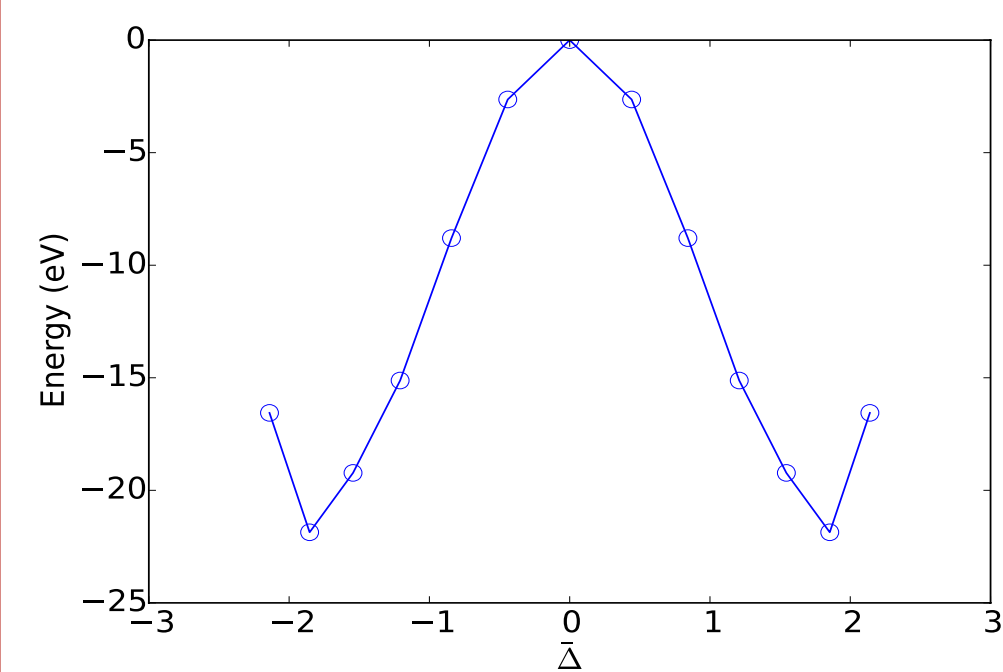
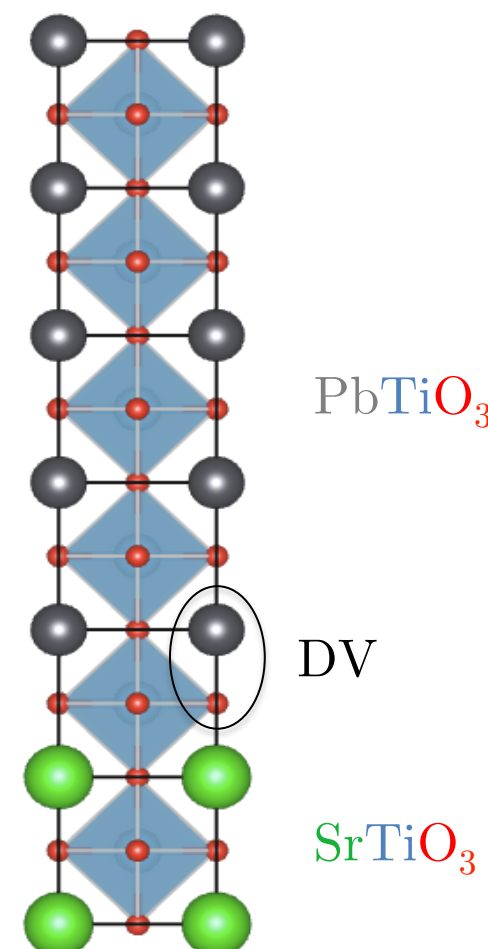


Motivation

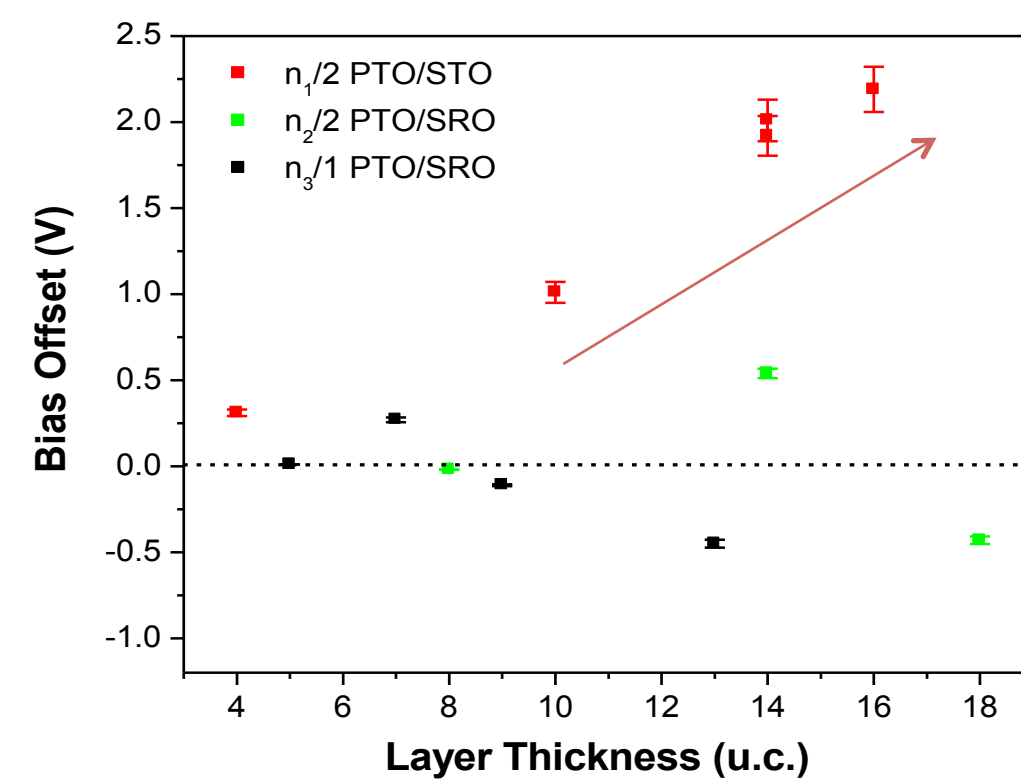
Thin film capacitors have an encouraging outlook on being used as memory storage devices over flash memory, primarily due to their low energy consumption and write speed. Lead titanate is a suitable candidate for such devices primarily due to its large switching charge and low process temperature. Furthermore, properties of the capacitor can be enhanced when synthesized as a superlattice with one or two materials. While a superlattice constructed from lead titanate and strontium titanate has improved dielectric properties, there is an unwanted presence of a voltage bias in the dielectric measurements of uncharted origin. In this work we use first principles calculations to outline why the bias exists as a first step to developing a solution to mitigate this problem.

5/2 $\text{PbTiO}_3/\text{SrTiO}_3$ superlattice, where each unit cell has a perovskite structure. The perovskite structure has a Pb/Sr ion on the edges, an O ion on the faces and a Ti ion at the center.



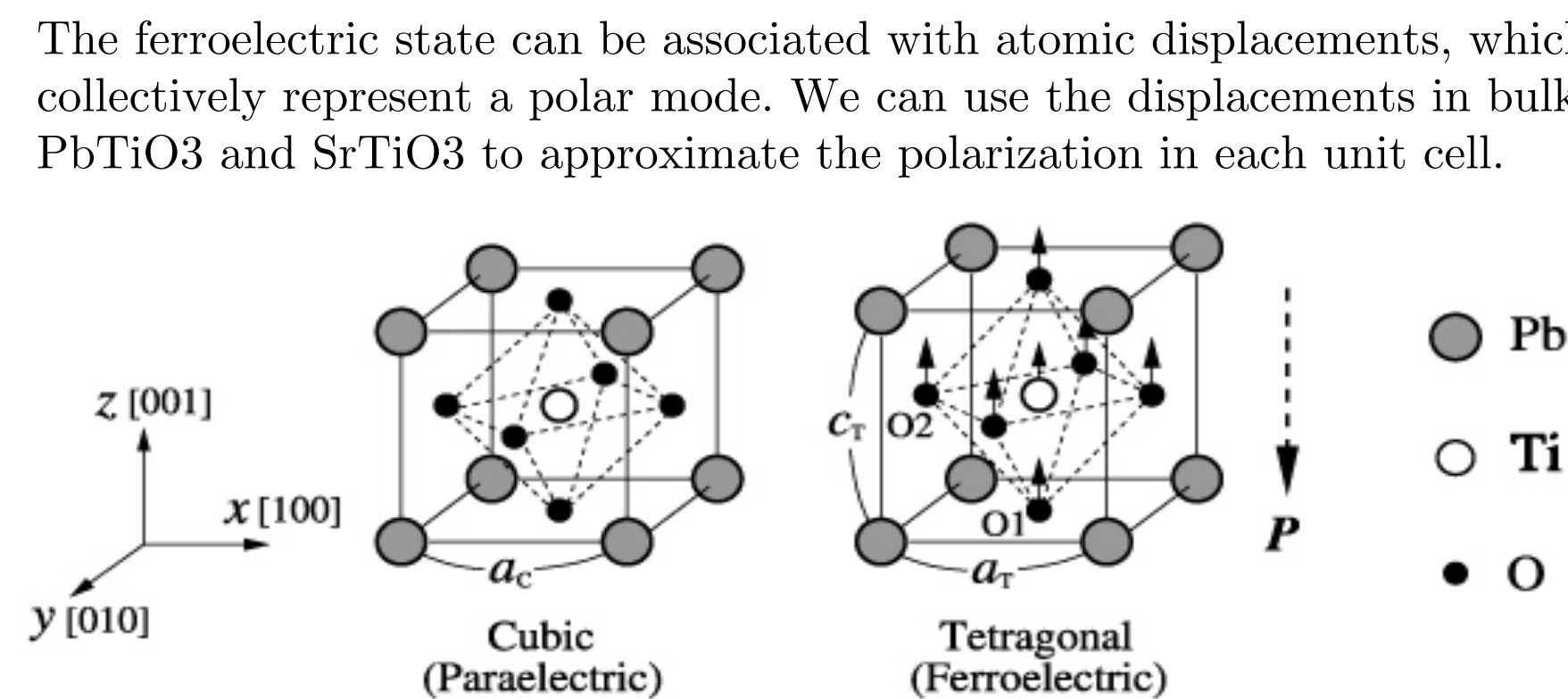
Typical potential energy surface of an ideal $\text{PbTiO}_3/\text{SrTiO}_3$ superlattice. The two equivalent ground states correspond to up and down polarizations.

Voltage bias from dielectric measurements seen in experiments. $\text{PbTiO}_3/\text{SrTiO}_3$ systems exhibit a larger bias with increasing PbTiO_3 thickness. This behavior is absent for $\text{PbTiO}_3/\text{SrRuO}_3$ systems.



Methods

We performed first principles calculations using density functional theory, where we simulated $\text{PbTiO}_3/\text{SrTiO}_3$ with a divacancy (a source of asymmetry) in either the interface between the PbTiO_3 and SrTiO_3 or in the center of the PbTiO_3 . It is important to note that the divacancy in the interface can only be formed with only one orientation and this is the same orientation that we use when it is formed in the PbTiO_3 for $\text{PbTiO}_3/\text{SrTiO}_3$. For $\text{PbTiO}_3/\text{SrRuO}_3$, we looked at both orientations due to the presence of inversion symmetry breaking at the interface. The bias that is present in experiments can be roughly translated to the energy difference between the two stable polarization states.

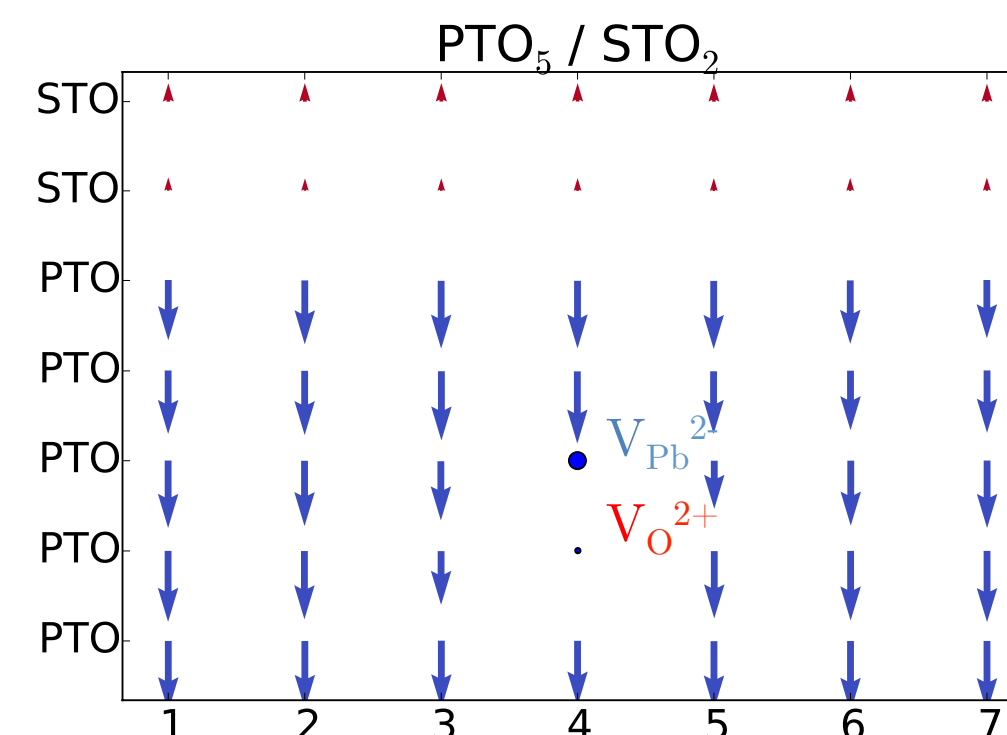


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The parameter ξ corresponds to the inner product of the displacements in the superlattice, Q , with the same type of displacements in the bulk, Q_0 . Therefore a value of 1 would correspond to polarization that would occur in bulk PbTiO_3 .

$$\xi = \frac{\mathbf{Q} \cdot \mathbf{Q}_0}{Q_0^2} \quad \mathbf{Q} = \begin{pmatrix} \Delta P_b_z \\ \Delta T_i_z \\ \Delta O_{1z} \\ \vdots \end{pmatrix}$$

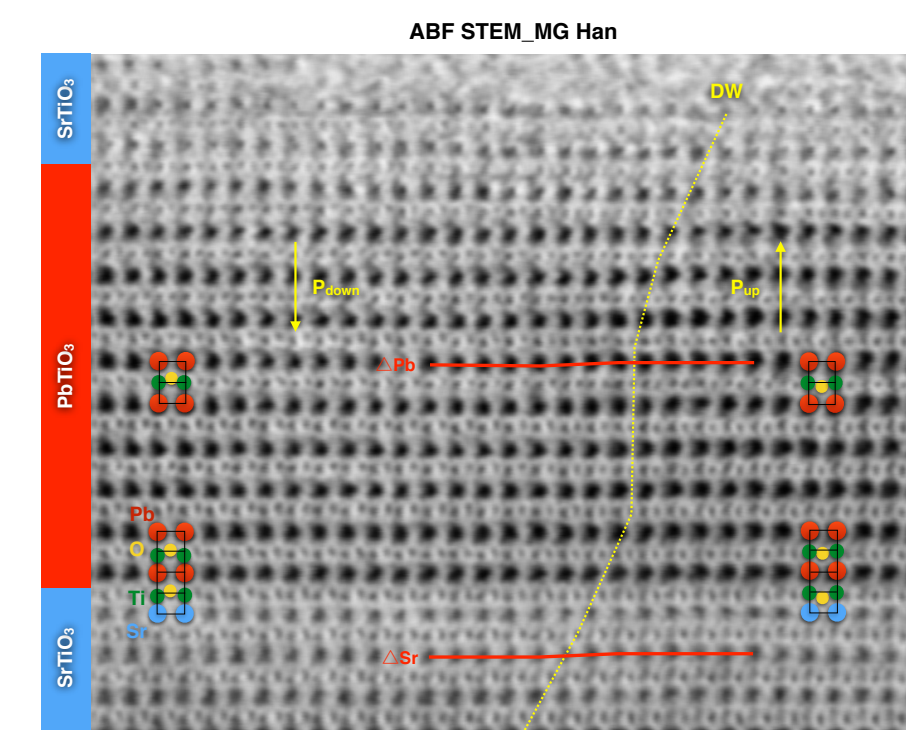
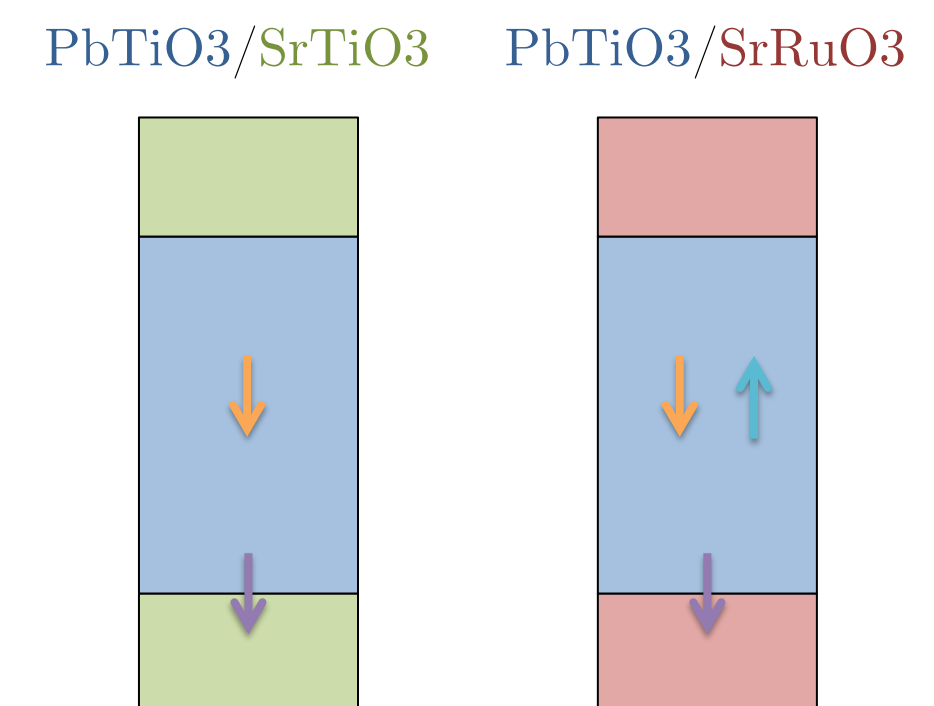
The plot of ξ per $\sqrt{2} \times \sqrt{2}$ unit cell shows that near the divacancy (represented by empty points) the polarization takes on relatively large values to screen the polarization of the dipole and smaller values in the SrTiO_3 due to an energy cost associated with polarizing the dielectric.



Results and Conclusion

Our results indicate that the asymmetry in polarization present due to the formation of the divacancy behaves differently in $\text{PbTiO}_3/\text{SrTiO}_3$ and $\text{PbTiO}_3/\text{SrRuO}_3$. In $\text{PbTiO}_3/\text{SrTiO}_3$ the ground state of the divacancy is almost degenerate, either forming in the PbTiO_3 or at the interface. However, in $\text{PbTiO}_3/\text{SrRuO}_3$ the divacancy prefers to sit in the PbTiO_3 , oriented with the preferred polarization direction stemming from the inversion symmetry breaking.

Parallel (meV)	Anti-Parallel (meV)	Δ (meV)
0	420	420
44	175	131
Parallel (meV)	Anti-Parallel (meV)	Δ (meV)
883	320	-563
0	715	715
551	1263	712



This STM image gives a possible insight into why the vacancies want to form at the interface rather than the PbTiO_3 . When the SrTiO_3 is grown on PbTiO_3 structural relaxations occur to account for the epitaxial strain and thus these relaxations change the energy landscape to favor further defects such as vacancies.

In the future we plan to do both experimental and simulation work on tricolor $\text{PbTiO}_3/\text{SrTiO}_3/\text{SrRuO}_3$, which should have the dielectric properties of $\text{PbTiO}_3/\text{SrTiO}_3$ and the lack of voltage bias as in $\text{PbTiO}_3/\text{SrRuO}_3$. If these properties hold for the tricolor superlattice then that would give us additional confidence in our understanding of the defects in these systems.